



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

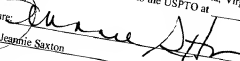
Docket No. 740819-408  
Serial No. 09/640,519  
Page 1

In re Patent Application of:  
Hiroaki NAKAOKA et al.  
Serial No. 09/640,519  
Filed: 08/17/2000  
For: METHOD OF FABRICATING  
SEMICONDUCTOR DEVICE

)  
) Group Art Unit: 2818  
) Examiner: David Vu  
) Confirmation No. 6287  
) Date: October 6, 2003

CERTIFICATE OF MAILING OR TRANSMISSION  
[37 CFR 1.8(a)]

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Signature:   
Name: Jeanne Saxton

AMENDMENT

Mail Stop  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 4, 2003, please amend the above identified application as follows.

IN THE CLAIMS:

Please cancel claims 9 and 10 in their entirety without prejudice nor disclaimer of the subject matter set forth therein.

Please amend claim 1 as follows.

1. (Currently Amended) A method of fabricating a semiconductor device, the method comprising the steps of:

NVA276083.1

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